



INFORMATION DISCLOSURE STATEMENT	Case No.	32
	Serial No.	09/450,054
	Applicant:	A. V. Krishnamoorthy
	Filing Date:	November 29, 1999
	Group:	

U.S. PATENT DOCUMENTS

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date
	AA						
	AB						
	AC						
	AD						
	AE						

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation
	AF						
	AG						
	AH						

OTHER (including Author, Title, Date, Pertinent Pages, etc.)

<i>C108</i>	AI	A. L. Fisher et al, "Synchronizing Large VLSI Processor Arrays", IEEE Trans. Comp., Vol. C-34, No. 8, pp. 734-740 (August 1985)
<i>C108</i>	AJ	A. V. Krishnamoorthy et al, "Vertical-Cavity Surface Emitting Lasers Flip-Chip Bonded to Gigabit/s CMOS Circuits", IEEE Photonics Technology Letters, Vol. 11, No. 1, January 1999
	AK	
	AL	
	AM	
	AN	

EXAMINER <i>C108</i>	DATE CONSIDERED <i>12/13/01</i>
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609: Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.